

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Vignis 22313-1450
www.uspto.gov

Bib Data Sheet

CONFIRMATION NO. 3470

SERIAL NUMBER 10/766,733	FILING OR 371(c) DATE 01/27/2004 RULE	CLASS 134	GROUP AR	ROUP ART UNIT 1746		ATTORNEY DOCKET NO. LAM1P108D	
Mike Ravkin, Don E. Ander ** CONTINUING DA This applicati ** FOREIGN APPLI IF REQUIRED, FOR	khaylich, San Jose, CA; Sunnyvale, CA; rson, Morgan Hill, CA; ATA ***********************************	06/10/1999 PAT 6,711	,775				
RS USC 110 (2-d) conditions		CA	SHEETS TOT DRAWING CLAI 9 15		MS	INDEPENDENT CLAIMS 2	
Michael L. Gencarella, Esq. Martine & Penilla, LLP. Suite 170 710 Lakeway Drive Sunnyvale, CA94085							
TITLE Method and system for cleaning a semiconductor wafer							
	ES: Authority has been gi to charge/cre	□ 1.1 □ 1.1 time) □ 1.1 □ 0th	All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time) 1.18 Fees (Issue) Other Credit				